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Conversely, boards that connect to Top Low and Bottom Low are known as “Low Memory”.DMX uses direct connections between directors and cache.When configuring cache for the Symmetrix DMX systems, follow these guidelines: • A minimum of four and a maximum of eight cache director boards is required for the DMX2000 and DMX3000 system configuration; and a minimum of two and a maximum of four cache director boards is required for the DMX1000 system configuration.

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